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Commissioner for Patents
POB 1450, Alexandria, Virginia 22313-1450
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RE: KAJIHARA, et al. - US Appl'n No. 09/987,978
Att'y Docket No. 501.32049RV1 / 329201392US4
Examiner J. Clark, AU 2815, USPTO Conf. No. 7905

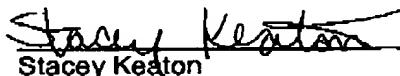
SUBMISSION OF AMENDMENT

Sir:

Applicant hereby submits the attached AMENDMENT (13 pages) for entry in
the above-noted application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached AMENDMENT (13 pages) is being
FORMALLY TRANSMITTED via the USPTO Main Facsimile No. 571-273-8300 on
November 20, 2006.



Stacey Keaton

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002/014
501.32049RV1/329201392US4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA et al.

Serial No.: 09/987,978

Filed: 16 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 7905

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

20 November 2006

Sir:

The following is respectfully submitted in connection with the above-identified application.

In accordance with 37 CFR §1.173, this reissue Amendment contains material to be added shown by underlining and material to be deleted contained within brackets. In the specification, the entire text of any rewritten paragraph is submitted with underlining/brackets, except any entire paragraph is deleted by a statement of cancellation without submission of the text of the paragraph. In the claims, the entire text of any rewritten original claim is submitted with brackets/underlining and a parenthetical status indicator, except any entire original claim is deleted by a statement of cancellation without submission of the text of the claim.